

**Program information**

Author : Author

Project name : Title

Version : 0.0

Module : SR2A201BD

Cycle time in the module : 7 x 2 ms

WATCHDOG action : Inactive

Type of Hardware Input Filtering : Slow (3ms)

 Zx keys inactive

Date format : dd/mm/yyyy



Program diagram

No	Contact 1	Contact 2	Contact 3	Contact 4	Contact 5	Coil	Comment
001	I1	I2	I3	t2	q2	[Q1	()
002	Stop	MV	Start V Q1			TT1	()
003			I4	t1	q1	[Q2	()
004			Start H Q2			TT2	()
005	Q1					[Q3	()
006	Q2					[Q4	()



Physical inputs

No	Symbol	Function	Lock	Parameters	Location of (L/C)	Comment
I1		Discrete inputs	--	No parameters	(1/1)	Stop
I2		Discrete inputs	--	No parameters	(1/2)	MV
I3		Discrete inputs	--	No parameters	(1/3)	Start V
I4		Discrete inputs	--	No parameters	(3/3)	Start H

Physical outputs

No	Symbol	Function	Latching	Location of (L/C)	Comment
Q1		Discrete outputs	No	(1/6) (2/3) (3/5) (5/1)	
Q2		Discrete outputs	No	(1/5) (3/6) (4/3) (6/1)	
Q3		Discrete outputs	No	(5/6)	
Q4		Discrete outputs	No	(6/6)	

Configurable functions

No	Symbol	Function	Lock	Latching	Parameters	Location of (L/C)	Comment
T1		Timers	No	No	See details below	(2/6) (3/4)	
T2		Timers	No	No	See details below	(1/4) (4/6)	

Timer

